



BCM846BSH-Q

65 V, 100 mA NPN/NPN matched double transistor

6 May 2021

Product data sheet

1. General description

NPN/NPN matched double transistor in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package.

PNP/PNP complement: BCM856BSH-Q

2. Features and benefits

- Low collector capacitance
- Low collector-emitter saturation voltage
- Current gain matching
- Base-emitter voltage matching
- Drop-in replacement for standard double transistors
- No mutual interference between the transistors
- High-temperature applications up to 175 °C
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Current mirror
- Differential amplifier

4. Quick reference data

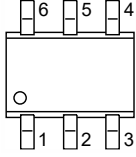
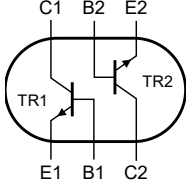
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per transistor						
V_{CE0}	collector-emitter voltage	open base	-	-	65	V
I_C	collector current		-	-	100	mA
h_{FE}	DC current gain	$V_{CE} = 5\text{ V}$; $I_C = 2\text{ mA}$; $T_{amb} = 25\text{ °C}$	200	300	450	
Per device						
h_{FE1}/h_{FE2}	DC current gain matching	$V_{CE} = 5\text{ V}$; $I_C = 2\text{ mA}$; $T_{amb} = 25\text{ °C}$		0.95	1	1.05
$V_{BE1}-V_{BE2}$	base-emitter voltage matching		[1]	-	-	2 mV

[1] The smaller of the two values is subtracted from the larger value.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	 TSSOP6 (SOT363)	 sym140
2	B1	base TR1		
3	C2	collector TR2		
4	E2	emitter TR2		
5	B2	base TR2		
6	C1	collector TR1		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BCM846BSH-Q	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BCM846BSH-Q	7N%

[1] % = placeholder for manufacturing site code

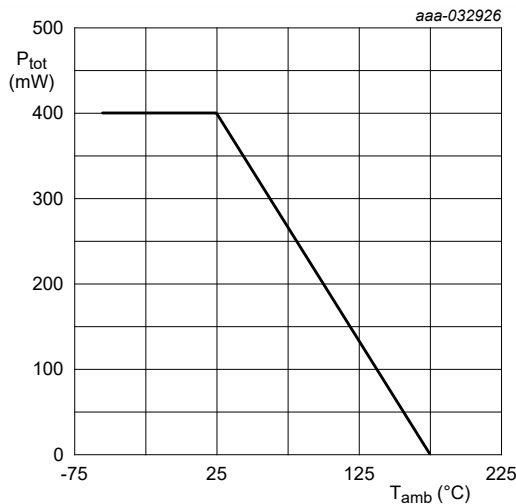
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transistor						
V_{CBO}	collector-base voltage	open emitter		-	80	V
V_{CEO}	collector-emitter voltage	open base		-	65	V
V_{EBO}	emitter-base voltage	open collector		-	7	V
I_C	collector current			-	100	mA
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms		-	200	mA
I_{BM}	peak base current			-	200	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	270	mW
Per device						
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	400	mW
T_j	junction temperature			-	175	°C
T_{amb}	ambient temperature			-55	175	°C
T_{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.



FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint

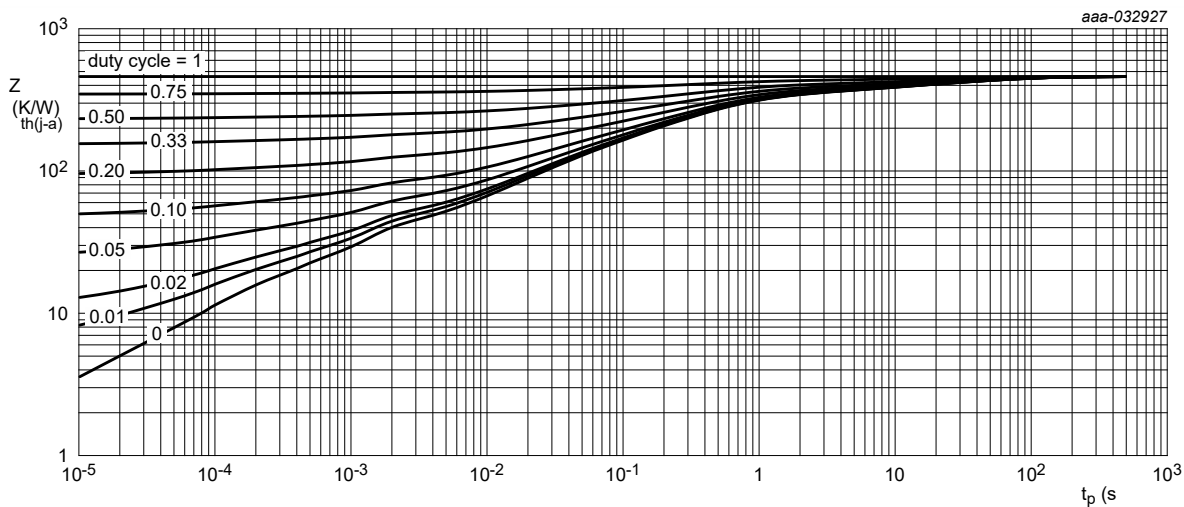
Fig. 1. Per device: Power derating curve

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per transistor							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	556	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	170	K/W
Per device							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	375	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.



FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint

Fig. 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per transistor							
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A; T _{amb} = 25 °C		80	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _B = 0 A; T _{amb} = 25 °C		65	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _C = 0 A; I _E = 100 μA; T _{amb} = 25 °C		7	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C		-	-	15	nA
		V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 7 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C		200	300	450	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C		-	50	100	mV
		I _C = 100 mA; I _B = 5 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C		-	200	300	mV
V _{BEsat}	base-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	[1]	-	750	850	mV
		I _C = 100 mA; I _B = 5 mA; T _{amb} = 25 °C		-	875	-	mV
V _{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	[2]	600	655	700	mV
		V _{CE} = 5 V; I _C = 10 mA; T _{amb} = 25 °C	[2]	-	705	770	mV
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	1.2	-	pF
C _e	emitter capacitance	V _{EB} = 0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	11	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C		100	-	-	MHz
NF	noise figure	V _{CE} = 5 V; I _C = 0.2 mA; R _S = 2 kΩ; f = 10 Hz to 15.7 kHz; T _{amb} = 25 °C		-	1.7	-	dB
		V _{CE} = 5 V; I _C = 0.2 mA; R _S = 2 kΩ; f = 1 kHz; B = 200 Hz; T _{amb} = 25 °C		-	3.1	-	dB
Per device							
h _{FE1} /h _{FE2}	DC current gain matching	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C		0.95	1	1.05	
V _{BE1} -V _{BE2}	base-emitter voltage matching		[3]	-	-	2	mV

[1] V_{BEsat} decreases by about 1.7 mV/K with increasing temperature.

[2] V_{BE} decreases by about 2 mV/K with increasing temperature.

[3] The smaller of the two values is subtracted from the larger value.

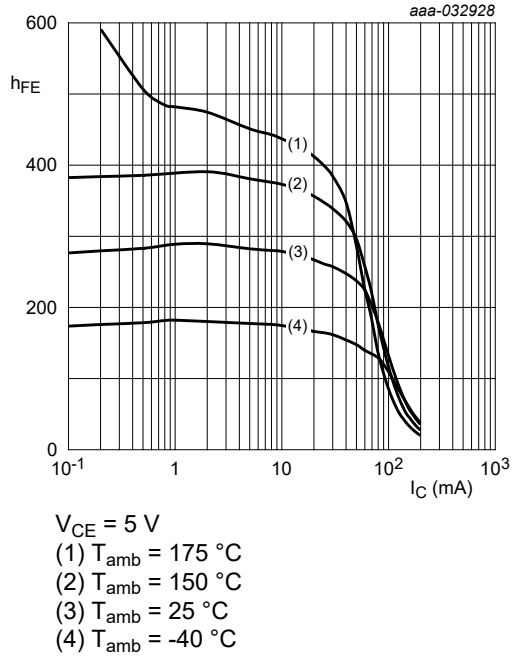


Fig. 3. DC current gain as a function of collector current; typical values

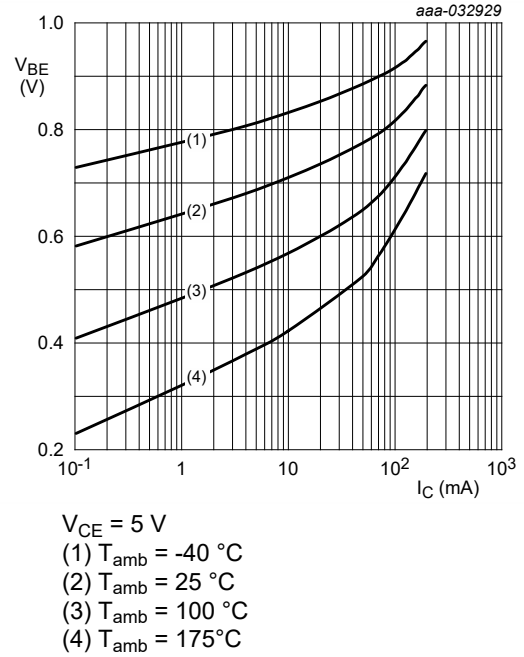


Fig. 4. Base-emitter voltage as a function of collector current; typical values

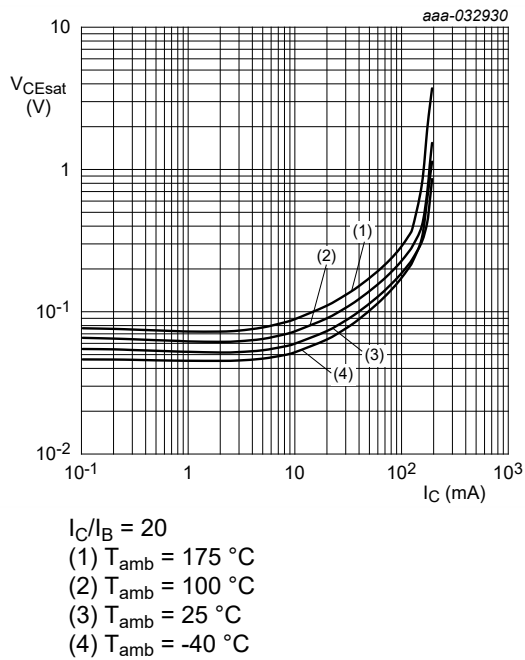


Fig. 5. Collector-emitter saturation voltage as a function of collector current; typical values

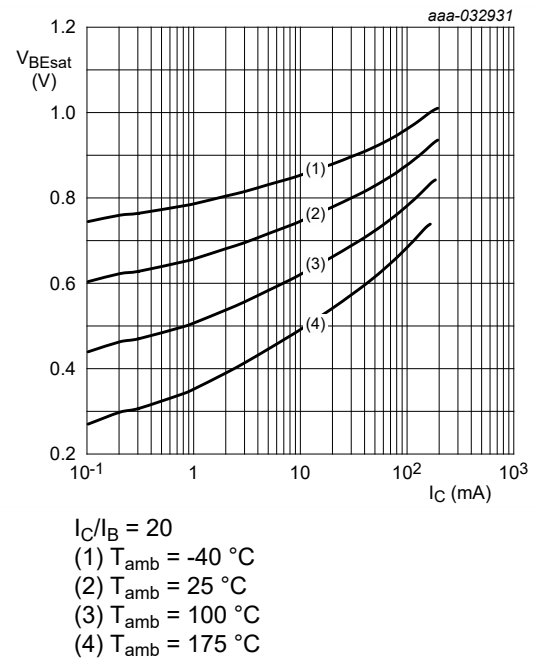


Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

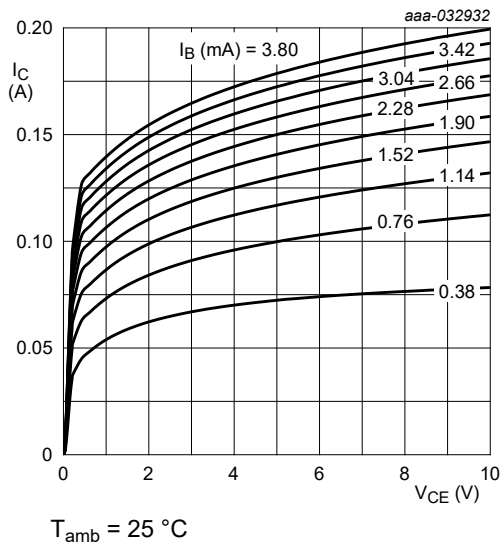


Fig. 7. Collector current as a function of collector-emitter voltage; typical values

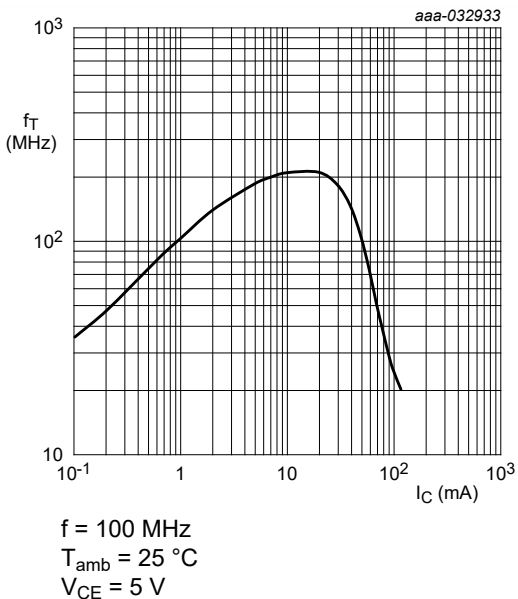


Fig. 8. Transition frequency as a function of collector current; typical values

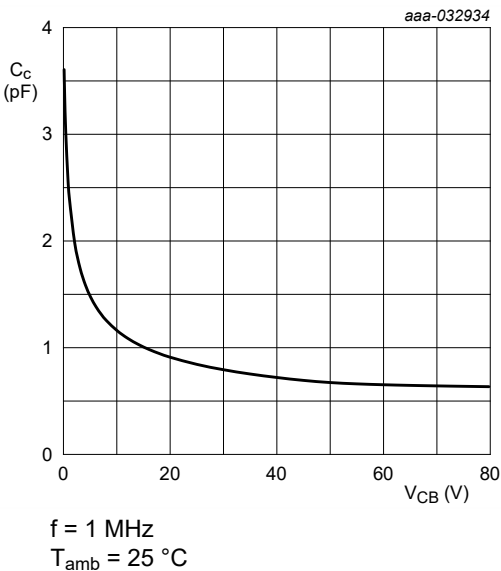


Fig. 9. Collector capacitance as a function of collector-base voltage; typical values

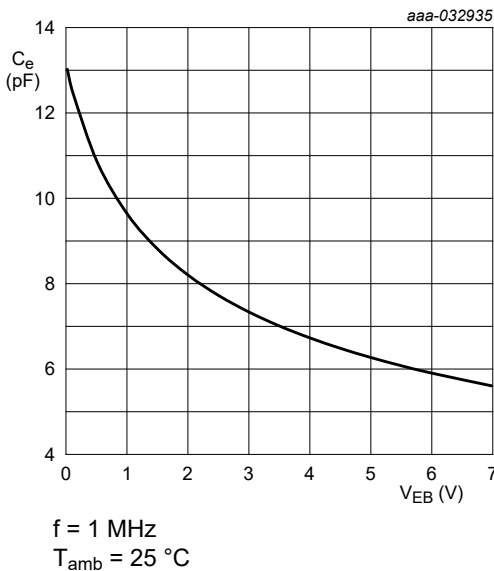


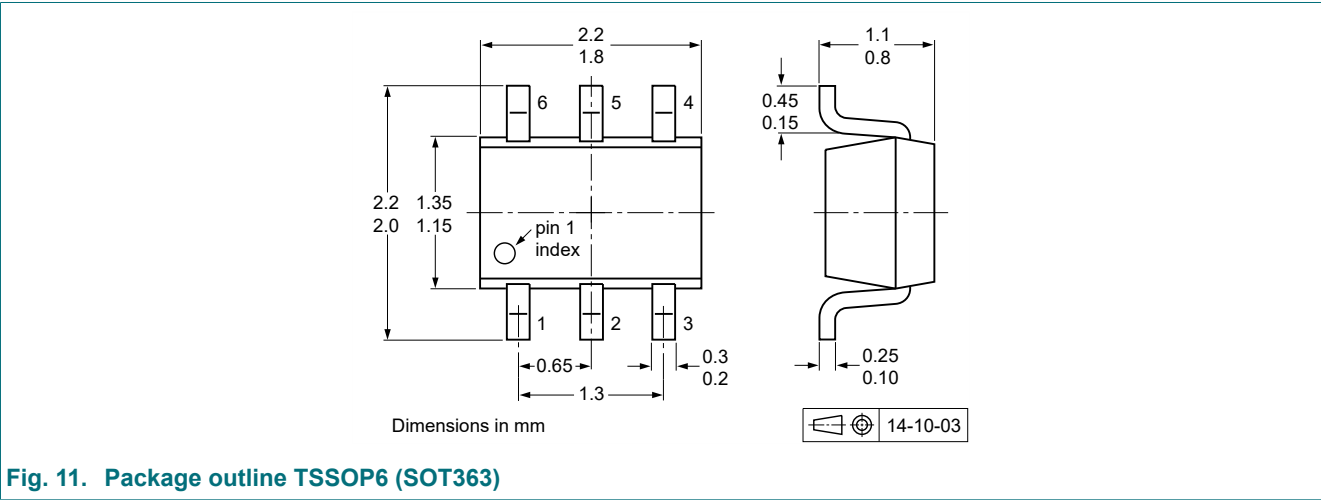
Fig. 10. Emitter capacitance as a function of emitter-base voltage; typical values

11. Test information

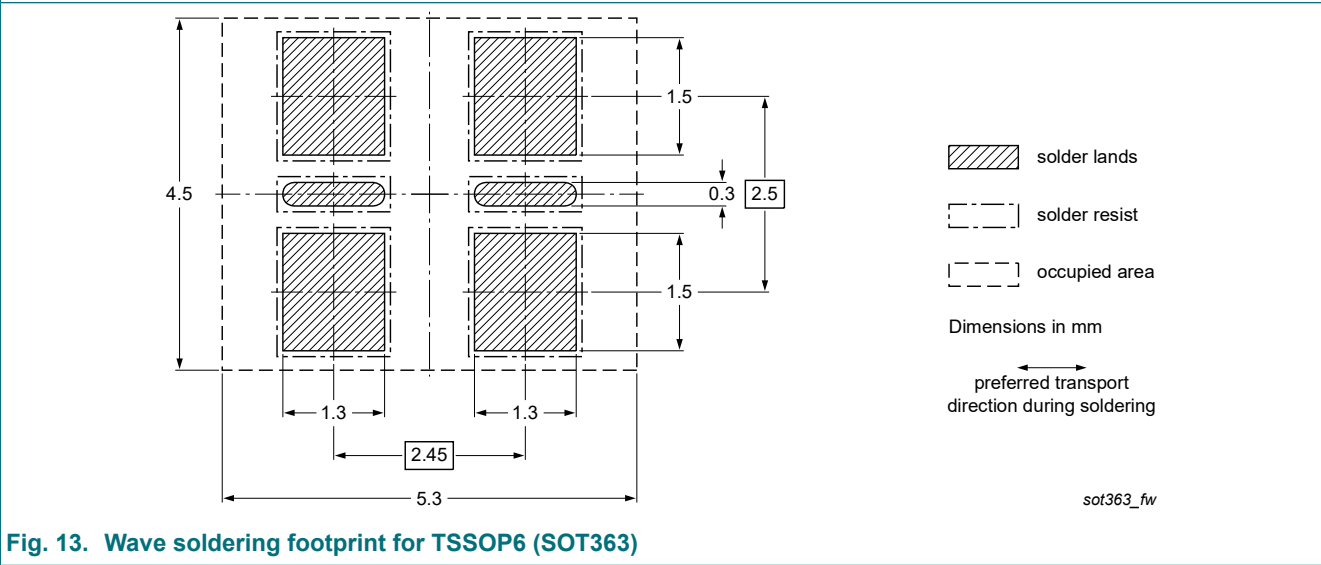
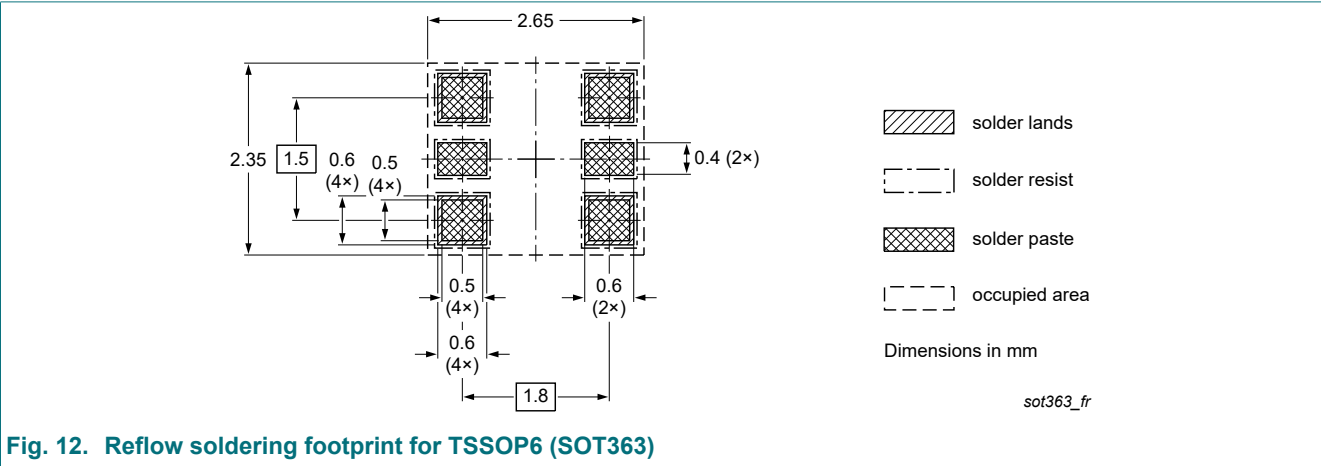
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCM846BSH-Q v.1	20210506	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. General description.....	1
2. Features and benefits.....	1
3. Applications.....	1
4. Quick reference data.....	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values.....	2
9. Thermal characteristics.....	3
10. Characteristics.....	4
11. Test information.....	6
12. Package outline.....	7
13. Soldering.....	7
14. Revision history.....	8
15. Legal information.....	9

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