DATA SHEET

BYV42E, BYV42EB series
Rectifier diodes ultrafast, rugged

Product specification

September 2018

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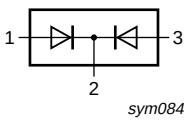
Rectifier diodes ultrafast, rugged

BYV42E, BYV42EB series

FEATURES

- Low forward volt drop
- Fast switching
- Soft recovery characteristic
- Reverse surge capability
- High thermal cycling performance
- Low thermal resistance

SYMBOL



sym084

QUICK REFERENCE DATA

V_R = 150 V/ 200 V

$V_F \leq 0.85$ V

$I_{O(AV)}$ = 30 A

I_{RRM} = 0.2 A

$t_{rr} \leq 28$ ns

GENERAL DESCRIPTION

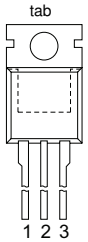
Dual, ultra-fast, epitaxial rectifier diodes intended for use as output rectifiers in high frequency switched mode power supplies.

The BYV42E series is supplied in the SOT78 conventional leaded package.
The BYV42EB series is supplied in the SOT404 surface mounting package.

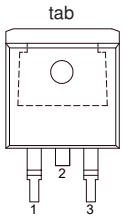
PINNING

PIN	DESCRIPTION
1	anode 1 (a)
2	cathode (k) [1]
3	anode 2 (a)
tab	cathode (k)

SOT78 (TO220AB)



SOT404



D2PAK (SOT404)

LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT
		BYV42E / BYV42EB		-150	-200	
V_{RRM}	Peak repetitive reverse voltage		-	150	200	V
V_{RWM}	Crest working reverse voltage		-	150	200	V
V_R	Continuous reverse voltage	$T_{mb} \leq 144$˚C	-	150	200	V
$I_{O(AV)}$	Average rectified output current (both diodes conducting)	square wave $\delta = 0.5$; $T_{mb} \leq 108$ ˚C	-	30		A
I_{FRM}	Repetitive peak forward current per diode	$t = 25$ µs; $\delta = 0.5$; $T_{mb} \leq 108$ ˚C	-	30		A
I_{FSM}	Non-repetitive peak forward current per diode	$t = 10$ ms	-	150		A
		$t = 8.3$ ms sinusoidal; with reapplied $V_{RWM(max)}$	-	160		A
I_{RRM}	Repetitive peak reverse current per diode	$t_p = 2$ µs; $\delta = 0.001$	-	0.2		A
I_{RSM}	Non-repetitive peak reverse current per diode	$t_p = 100$ µs	-	0.2		A
T_{stg}	Storage temperature		-40	150		˚C
T_j	Operating junction temperature		-	150		˚C

1. It is not possible to make connection to pin 2 of the SOT404 package
2. SOT78 package, For output currents in excess of 20 A, the cathode connection should be made to the mounting tab.

ESD LIMITING VALUE

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_C	Electrostatic discharge capacitor voltage	Human body model; C = 250 pF; R = 1.5 kΩ	-	8	kV

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j\text{-}mb}$	Thermal resistance junction to mounting base	per diode	-	-	2.4	K/W
		both diodes	-	-	1.4	K/W
$R_{th\ j\text{-}a}$	Thermal resistance junction to ambient	SOT78 package, in free air	-	60	-	K/W
		SOT404 and SOT428 packages, pcb mounted, minimum footprint, FR4 board	-	50	-	K/W

ELECTRICAL CHARACTERISTICS

characteristics are per diode at T_j = 25 ˚C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_F	Forward voltage	I_F = 15 A; T_j = 150˚C	-	0.78	0.85	V
		I_F = 15 A	-	0.95	1.05	V
		I_F = 30 A	-	1.00	1.20	V
I_R	Reverse current	$V_R = V_{RWM}$; T_j = 100 ˚C	-	0.5	1	mA
		$V_R = V_{RWM}$	-	10	100	µA
Q_s	Reverse recovery charge	I_F = 2 A; $V_R \geq$ 30 V; $-dI_F/dt$ = 20 A/µs	-	6	15	nC
t_{rr1}	Reverse recovery time	I_F = 1 A; $V_R \geq$ 30 V; $-dI_F/dt$ = 100 A/µs	-	20	28	ns
t_{rr2}	Reverse recovery time	I_F = 0.5 A to I_R = 1 A; I_{rec} = 0.25 A	-	13	22	ns
V_{fr}	Forward recovery voltage	I_F = 1 A; dI_F/dt = 10 A/µs	-	1	-	V

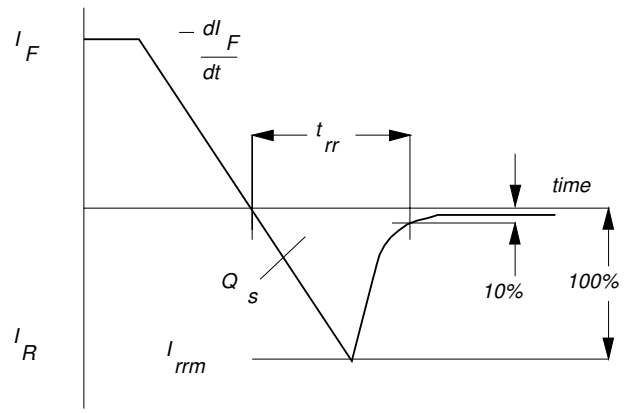


Fig.1. Definition of t_{rr1}, Q_s and I_{rrm}

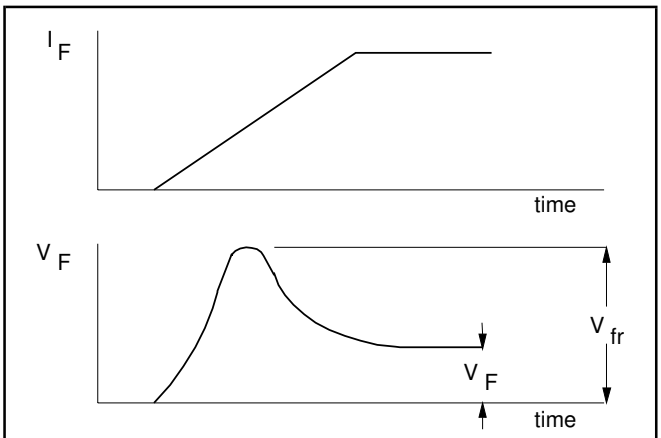


Fig.2. Definition of V_{fr}

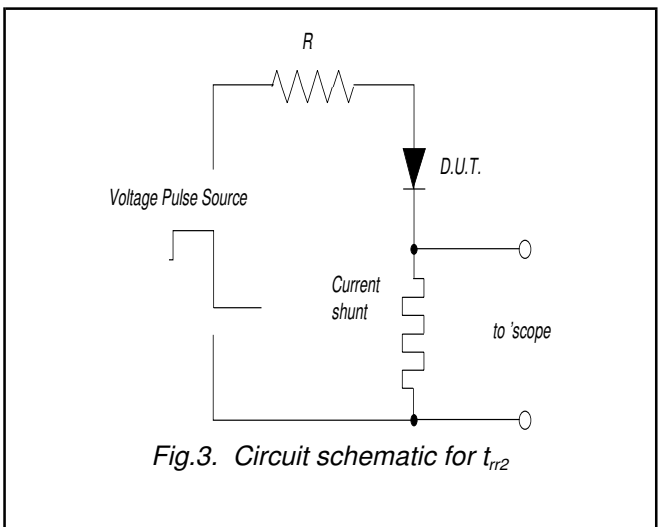


Fig.3. Circuit schematic for t_{rr2}

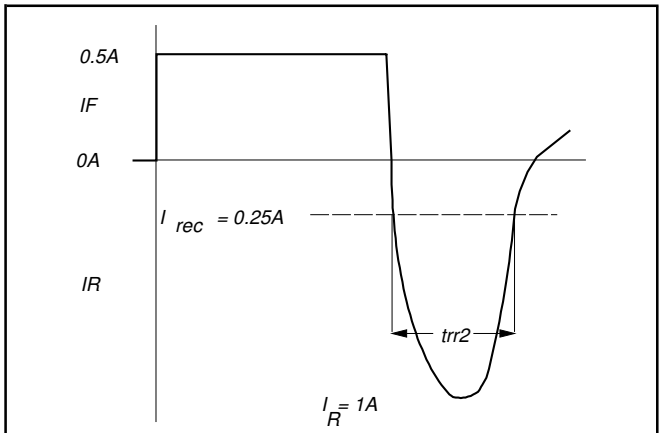


Fig.4. Definition of t_{rr2}

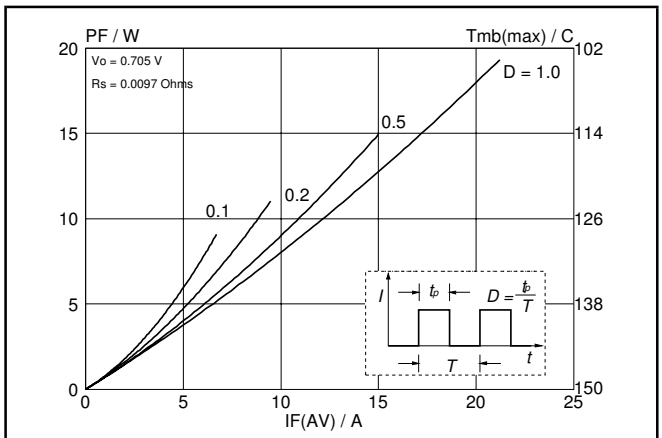


Fig.5. Maximum forward dissipation $P_F = f(I_{F(AV)})$ per diode; square current waveform where $I_{F(AV)} = I_{F(RMS)} \times \sqrt{D}$.

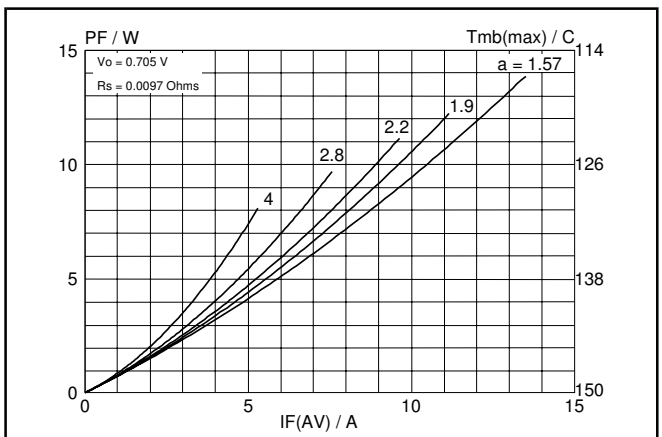


Fig.6. Maximum forward dissipation $P_F = f(I_{F(AV)})$ per diode; sinusoidal current waveform where a = form factor = $I_{F(RMS)} / I_{F(AV)}$.

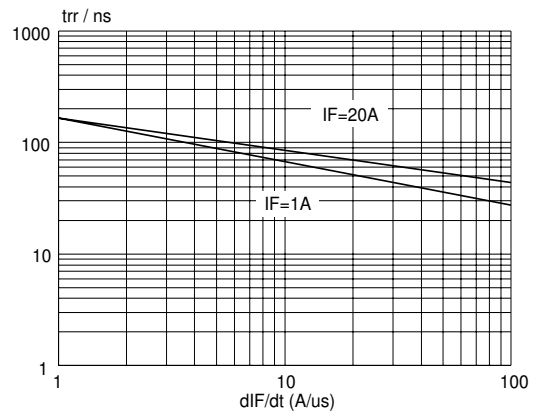


Fig.7. Maximum t_{rr} at T_j = 25 ˚C; per diode

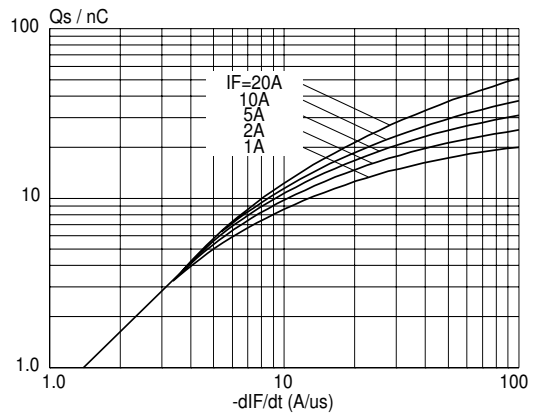


Fig.10. Maximum Q_s at T_j = 25 ˚C; per diode

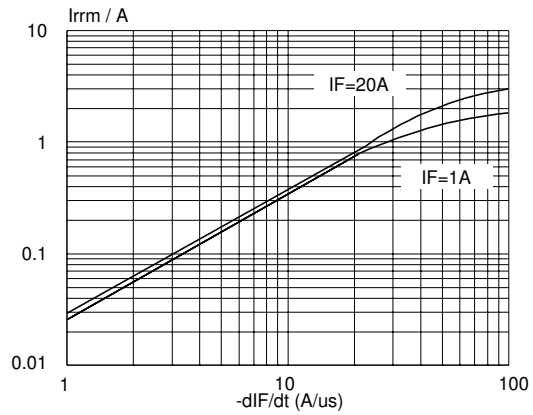


Fig.8. Maximum I_{rrm} at T_j = 25 ˚C; per diode

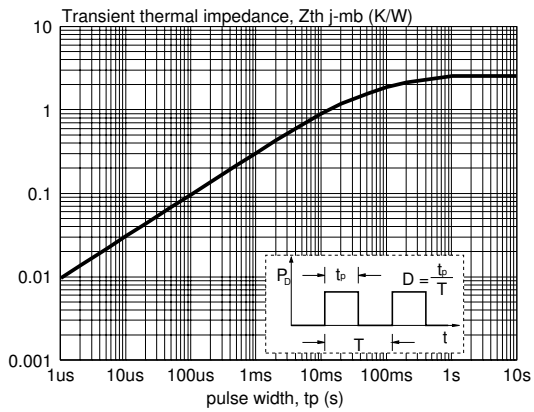


Fig.11. Transient thermal impedance; per diode; $Z_{th\ j\text{-}mb} = f(t_p)$.

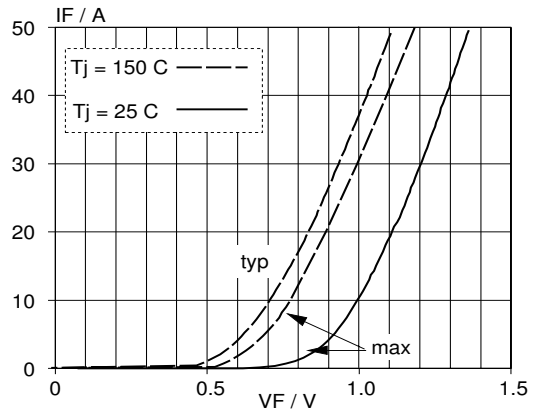
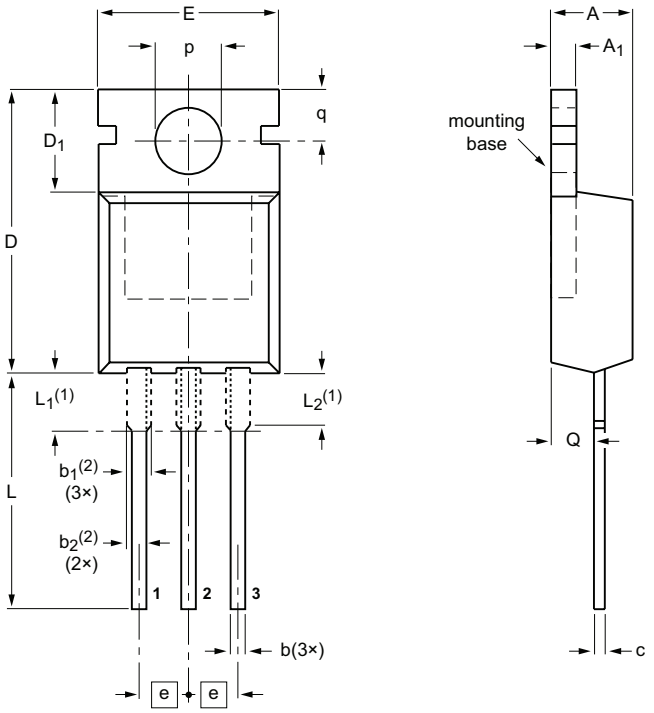


Fig.9. Typical and maximum forward characteristic $I_F = f(V_F)$; parameter T_j

MECHANICAL DATA

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB **SOT78**



DIMENSIONS (mm are the original dimensions)

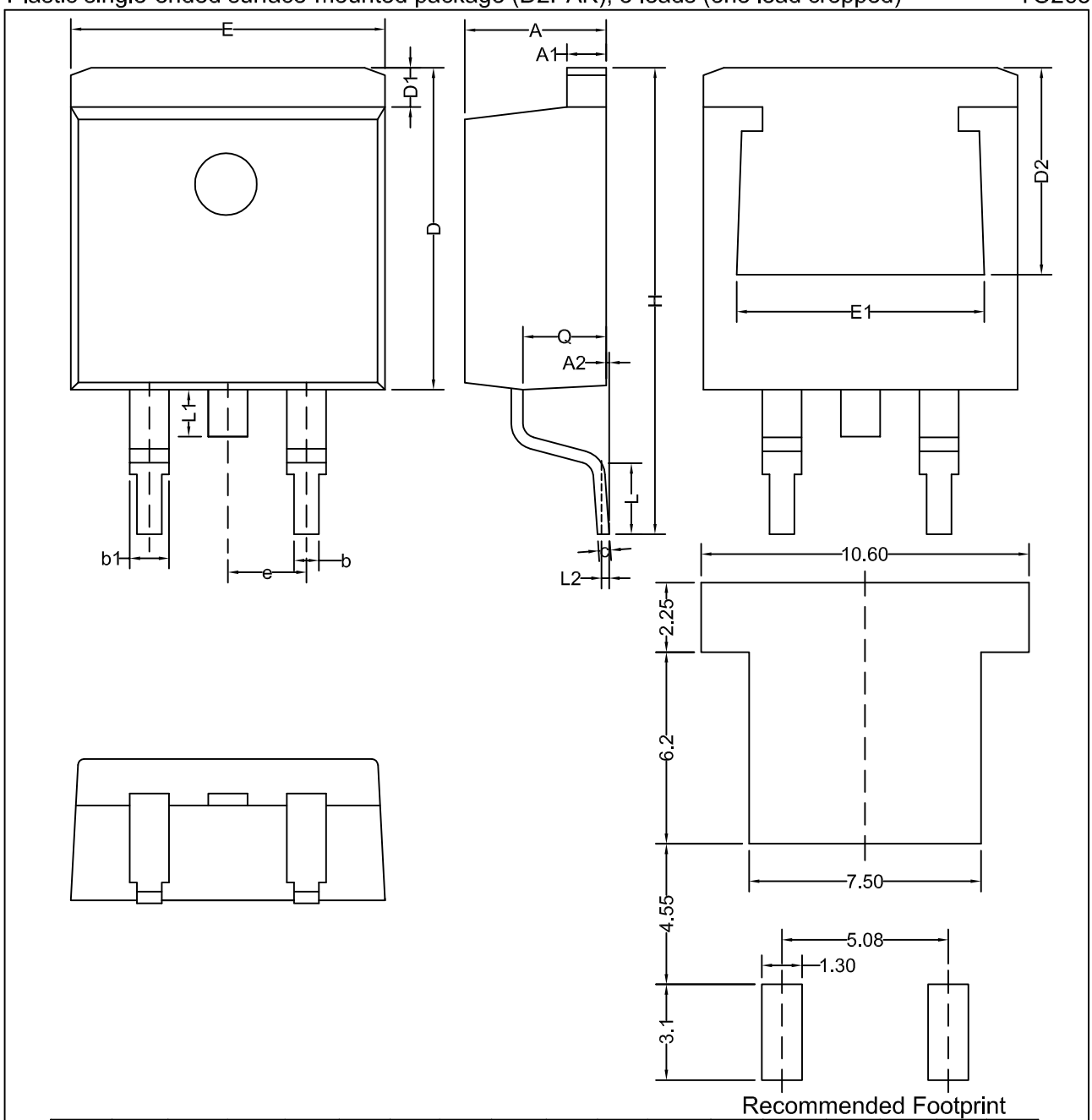
UNIT	A	A_1	b	b_1(2)	b_2(2)	c	D	D_1	E	e	L	L_1(1)	L_2(1) max.	p	q	Q
mm	4.7 4.1	1.40 1.25	0.9 0.6	1.6 1.0	1.3 1.0	0.7 0.4	16.0 15.2	6.6 5.9	10.3 9.7	2.54	15.0 12.8	3.30 2.79	3.0	3.8 3.5	3.0 2.7	2.6 2.2

Notes
1. Lead shoulder designs may vary.
2. Dimension includes excess dambar.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT78		3-lead TO-220AB	SC-46			~~08-04-23~~ 08-06-13

MECHANICAL DATA

Plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped) TO263



	A	A1	A2	b	b1	c	D	D1	D2	e	E	E1	H	L	L1	L2	Q
min	4.10	1.22	0.00	0.60	1.05	0.34	---	1.20	6.60	2.54 (BSC)	9.70	7.80	14.80	2.10	---	0.25 (BSC.)	2.20
max	4.70	1.40	0.25	0.90	1.45	0.64	11.00	1.60	---		10.30	---	15.80	2.90	1.75		2.79

Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".
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